



SS8550

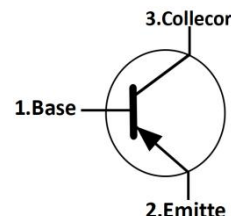
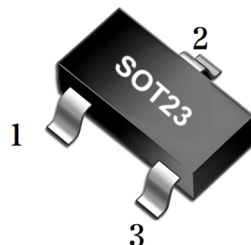
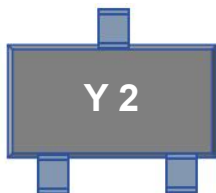
PNP SILICON TRANSISTOR

FEATURES

- * High Collector Current
- * Complementary to SS8050.

MARKING

Type Code: Marking: Y 2



ABSOLUTE MAXIMUM RATINGS (Tc=25°C, unless otherwise specified)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CB0}	Collector-base voltage	-40	V
V _{CE0}	Collector-emitter voltage	-25	V
V _{EB0}	Emitter-base voltage	-5	V
I _c	Collector current	-1.5	A
P _c	Collector Power Dissipation	0.3	W
T _j	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-55~150	°C
R _{θJA}	Thermal Resistance From Junction To Ambient	417	°C/W

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

ELECTRICAL CHARACTERISTICS (Tc=25°C, unless otherwise specified)

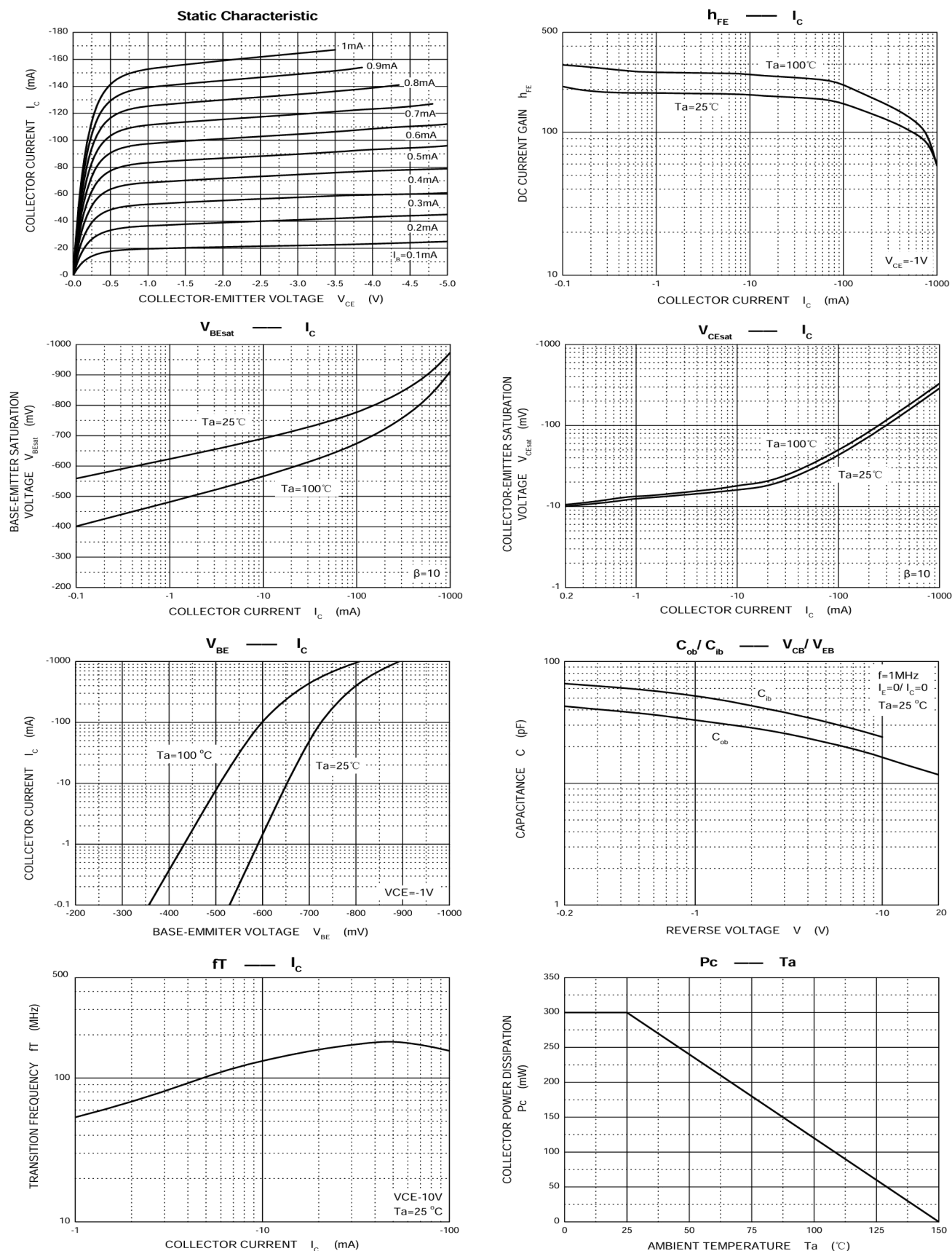
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =-0.1mA, I _E =0	-40			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =-0.1mA, I _B =0	-25			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =-0.1mA, I _C =0	-5			V
Collector cutoff current	I _{CBO}	V _{CB} =-40V, I _E =0			-0.1	μA
Collector cutoff current	I _{CEO}	V _{CE} =-20V, I _E =0			-0.1	μA
Emitter cutoff current	I _{EBO}	V _{EB} =-5V, I _C =0			-0.1	μA
DC Current Gain (CLASSIFICATION OF h _{FE1})	h _{FE1}	V _{CE} =-1V, I _C =-100mA	120		200	
			200		350	
			300		400	
	h _{FE2}	V _{CE} =-1V, I _C =-800mA	40			
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =-800mA, I _B =-80mA			-0.5	V
Base-emitter saturation voltage	V _{BE(sat)}	I _C =-800mA, I _B =-80mA			-1.2	V
Base-emitter voltage	V _{BE}	V _{BE} =-1V, I _C =-10mA			-1	V
Transition frequency	f _T	V _{CE} =-10V, I _C =-50mA, f=30MHz	100			MHz
Collector output capacitance	C _{ob}	V _{CB} =-10V, I _E =0, f=1MHz			20	pF



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TYPICAL CHARACTERISTICS

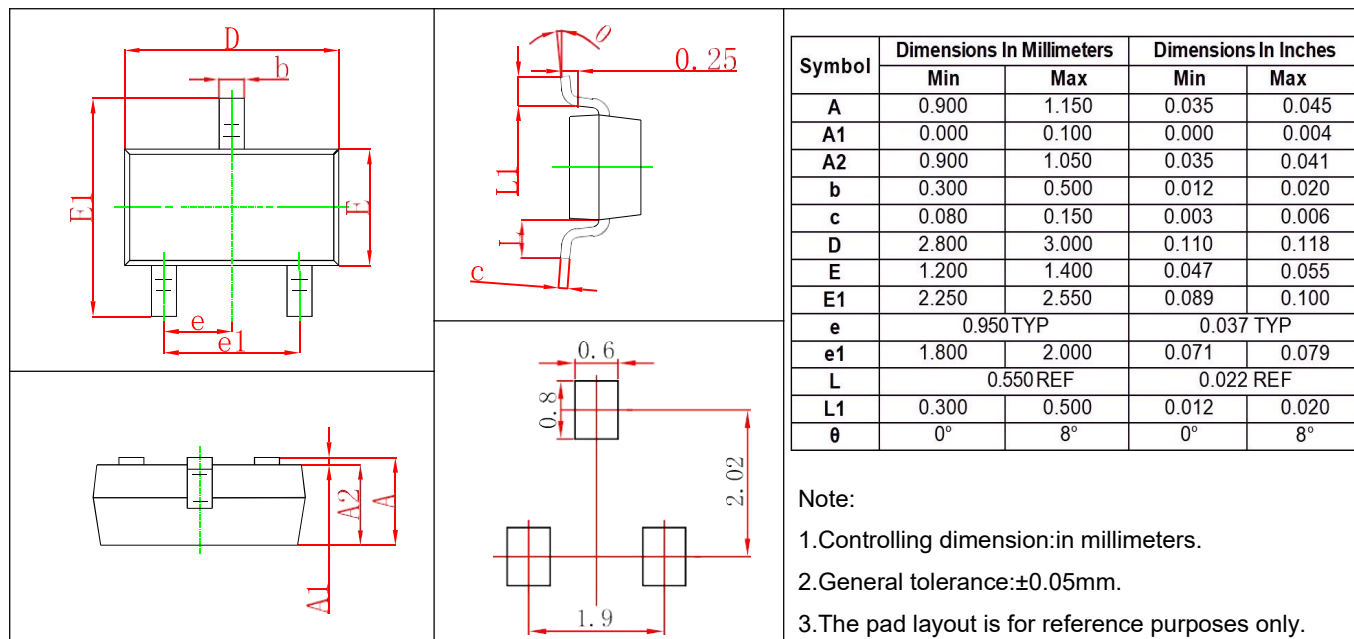




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SOT23 PACKAGE OUTLINE DIMENSIONS



REEL PACKING

