



## BTA08

TRIAC

### ■ GENERAL DESCRIPTION

\*The BTA08 is a 8A triacs which can be operated in 3 quadrants, it uses our advanced technology to provide customers with high commutation performances,etc.

\*The BTA08 is suitable for AC switching application and phase control application such as fan speed and temperature modulation control, lighting control and static switching relay, either in through-hole or surface-mount packages.

### ■ MARKING



: HY LOGO

BTA08=Device Code

XXXX=Date Code

Solid Dot=Green molding compound

### ■ ABSOLUTE MAXIMUM RATINGS (TC=25°C, unless otherwise specified)

SYMBOL	PARAMETER		TEST CONDITION	VALUE	UNIT
$V_{DRM}/V_{RRM}$	Repetitive Peak off-state/reverse voltage		$T_c=100^\circ\text{C}$	600	V
				800	V
$I_T(\text{RMS})$	RMS On-State Current(Full Sine Wave)		$T_c=100^\circ\text{C}$	8	A
$I_{TSM}$	Non Repetitive Surge Peak On-State Current ((Full Cycle $T_j$ initial=25°C))		$t=20\text{ms}; F=50\text{Hz}$	80	A
			$t=16.7\text{ms}; F=60\text{Hz}$	84	
$I_{GM}$	Peak Gate Current	$t_p=20\mu\text{s}$	$T_j=125^\circ\text{C}$	4	A
$I^2t$	$I^2t$ Value for Fusing		$t_p=10\text{ms}$	36	$\text{A}^2\text{s}$
$dI/dt$	Critical Rate of Rise of On-State Current: $I_G=2 \times LG_T$ , $t_r \leq 100\text{ns}$		$F=120\text{Hz}; T_j=125^\circ\text{C}$	50	$\text{A}/\mu\text{s}$
$P_{G(AV)}$	Average Gate Power Dissipation ( $T_j=125^\circ\text{C}$ )			1	W
$T_{stg}$	Storage Junction Temperature			-40 to +150	°C
$T_j$	Operating junction temperature			-40 to + 125	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.



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## ■ THERMAL RESISTANCES

PARAMETER		SYMBOL	RATINGS		UNIT
Junction to Ambient	TO-220F	R <sub>θJA</sub>	60		°C/W
	TO-252		70		
Junction to Case	TO-220F	R <sub>θJC</sub>	2.5		°C/W
	TO-252		1.6		

## ■ SENSITIVITY AND TYPE

PART NUMBER	VOLTAGE		SENSITIVITY	TYPE
	600V	800V		
B	◎	◎	50mA	STANDARD
C	◎	◎	25mA	STANDARD

◎ : Available

## ■ ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C unless otherwise noted)

FOR STANDARD (4 QUADRANTS)

PARAMETER	SYMBOL	TEST CONDITIONS		C			B			UNIT
				MIN	TYP	MAX	MIN	TYP	MAX	
Gate Trigger Current (Note 1)	I <sub>GT</sub>	V <sub>D</sub> =12V, R <sub>L</sub> =33Ω	T2+, G+			25			50	mA
			T2+, G -			25			50	
			T2-, G -			25			50	
Gate Trigger Voltage	V <sub>GT</sub>	V <sub>D</sub> =12V, R <sub>L</sub> =33Ω	T2+, G +			1.5			1.5	V
			T2+, G -			1.5			1.5	
			T2-, G -			1.5			1.5	
Gate Non-Trigger Voltage	V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> R <sub>L</sub> =3.3kΩ T <sub>j</sub> =125°C	I-II-III	0.2			0.2			V
Holding Current (Note 2)	I <sub>H</sub>	I <sub>T</sub> =500mA				25			50	mA
Latching Current	I <sub>L</sub>	I <sub>G</sub> =1.2I <sub>GT</sub>	I-I-III			40			50	mA
			II			80			100	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V <sub>D</sub> =67%V <sub>DRM</sub> , (Gate Open), T <sub>j</sub> =125°C		200			400			V/μs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dV/dt) <sub>C</sub>	(dI/dt) <sub>C</sub> =5.3A/ms, T <sub>j</sub> = 125°C		5			10			V/μs



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## ■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 1)	$V_{TM}$	$I_{TM}=11A, t_P=380\mu s$			1.50	V
Threshold Voltage (Note 2)	$V_{TO}$				0.92	V
Dynamic Resistance (Note 2)	$R_D$				36.6	mΩ
Repetitive Peak Off-State Current	$I_{DRM}$	$V_{DRM}=V_{RRM}$	$T_J=25^\circ C$		5	μA
	$I_{RRM}$		$T_J=125^\circ C$		0.5	mA

Notes: 1. Minimum  $I_{GT}$  is guaranteed at 5% of  $I_{GT}$  max.

2. For both polarities of MT2 referenced to MT1.

## ■ TYPICAL CHARACTERISTICS

Fig.1 Maximum Power Dissipation vs. RMS on-State Current (full cycle)

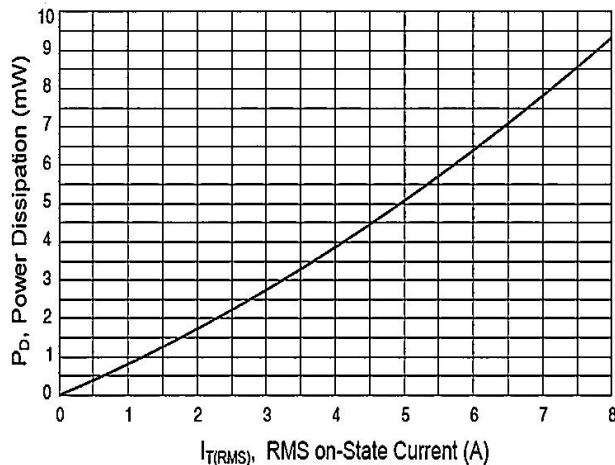


Fig.2 RMS on-State Current vs.Tc

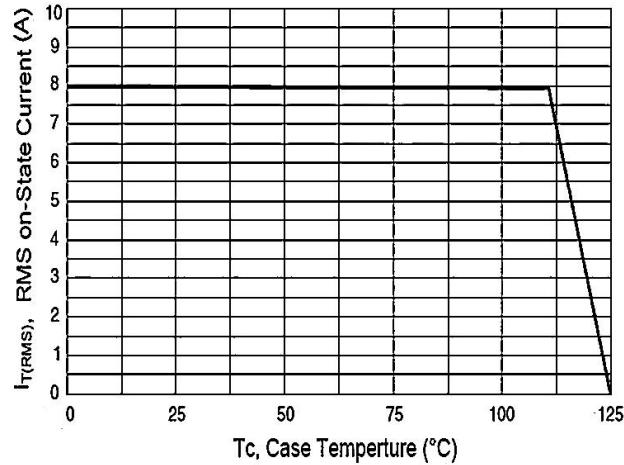


Fig.3 RMS on-state current vs.Ta

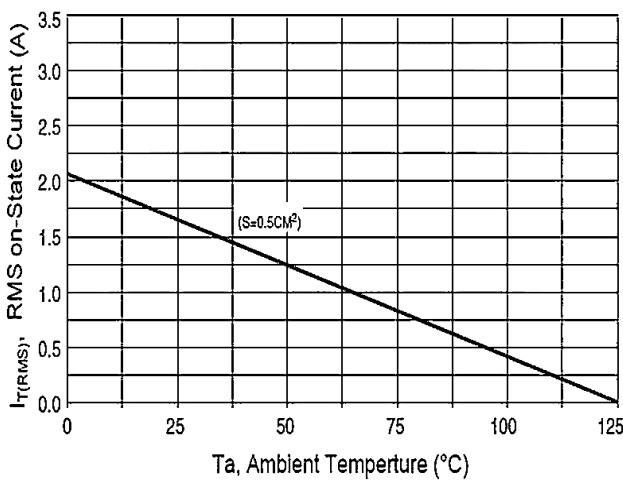
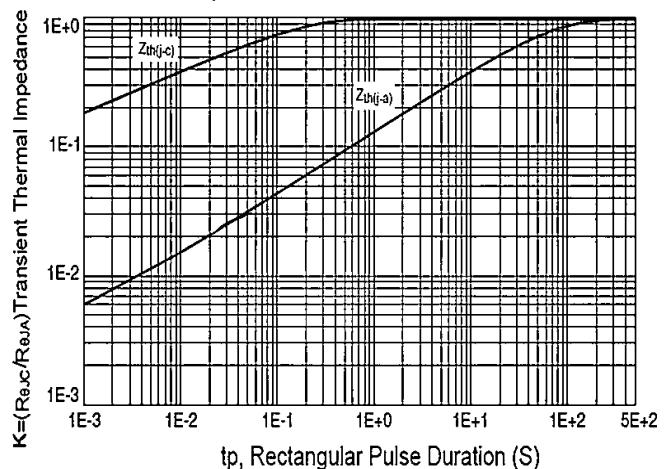


Fig.4 Relative Variation of Thermal Impedance vs. Pulse Duration





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## ■ TYPICAL CHARACTERISTICS(Con.t)

Fig.5 maximum on-State Characteristics

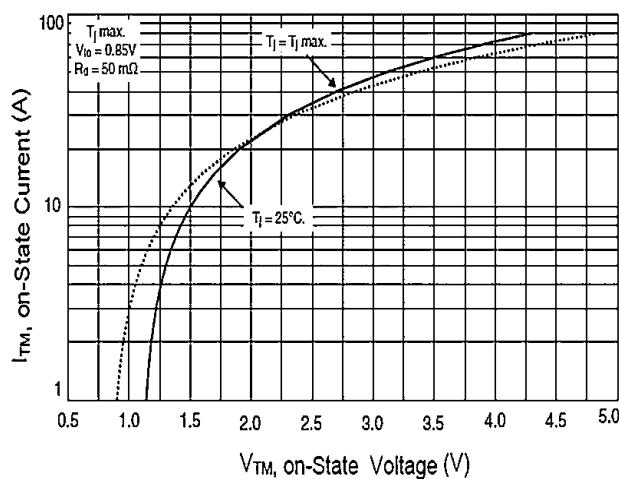


Fig.7 Typical Relative Variation of Gate Trigger Current, Holding Current and Latching Current vs.  $T_J$

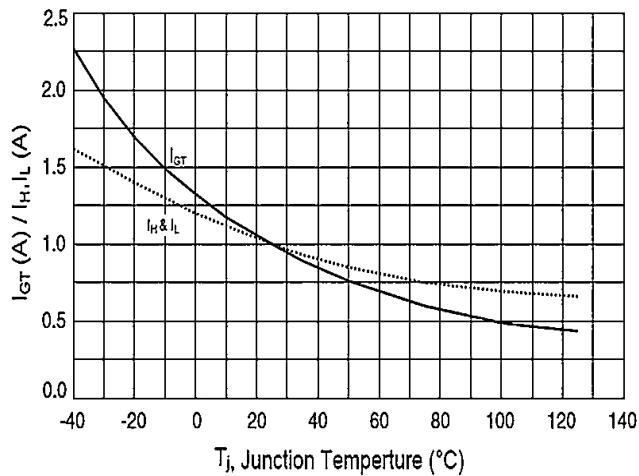


Fig.6 Surge Peak on-State Current vs. number of Cycles

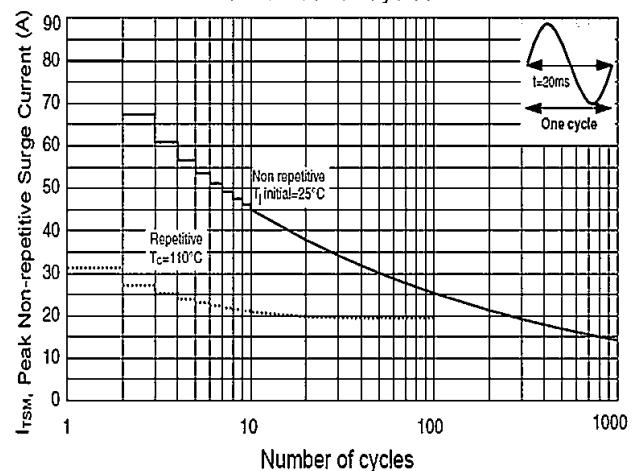
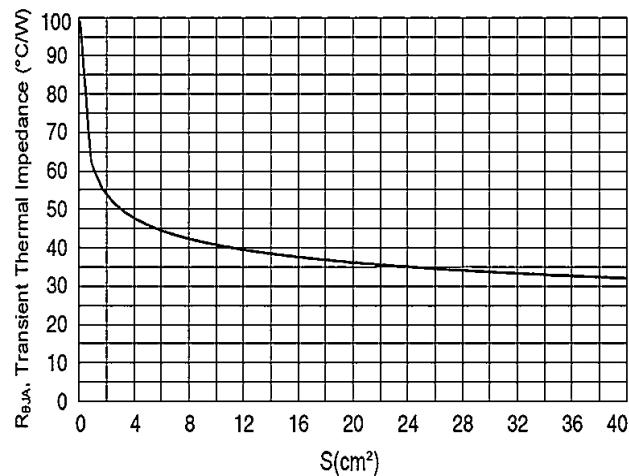


Fig.8 Thermal Resistance Junction to Ambient vs. Copper Surface under Tab

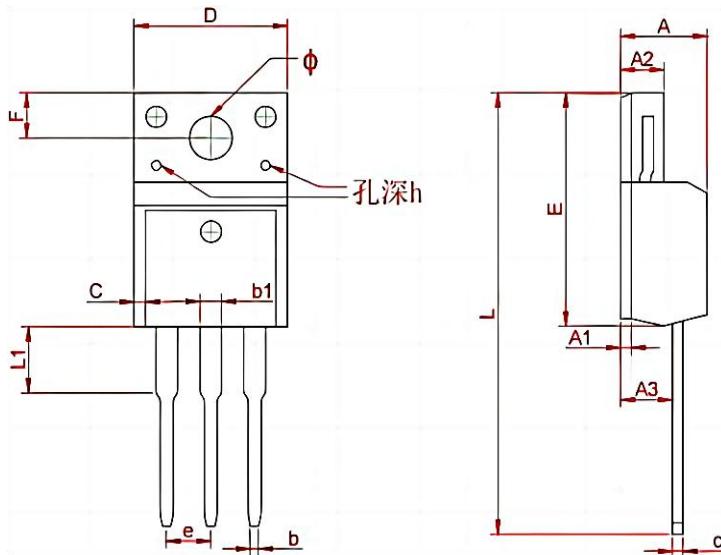




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### ■ TO - 220F PACKAGE OUTLINE DIMENSIONS

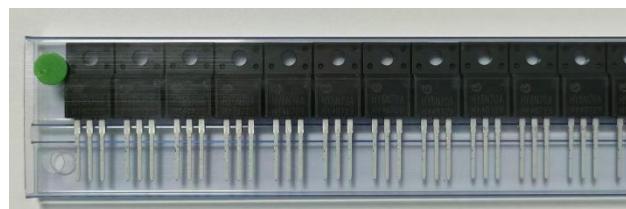


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max	Min	Max
A	4.300	4.750	0.169	0.185
A1	1.830	REF	0.072	REF
A2	2.300	2.850	0.090	0.112
A3	2.500	2.900	0.098	0.114
b	0.400	0.420	0.016	0.016
b1	1.220	1.280	0.048	0.050
C	0.690	0.720	0.027	0.028
c	0.490	0.510	0.019	0.020
D	9.960	10.200	0.392	0.400
E	15.000	15.950	0.588	0.625
e	2.574	TYP	0.101	TYP
F	3.470	REF	0.136	REF
y	3.200	REF	0.125	REF
h	0.000	0.300	0.000	0.012
L	28.780	28.900	1.128	1.133
L1	2.990	3.100	0.117	0.122

### ■ TO - 220F PACKING INFORMATION



50PCS →



← 5 Inner Box



20 Tube

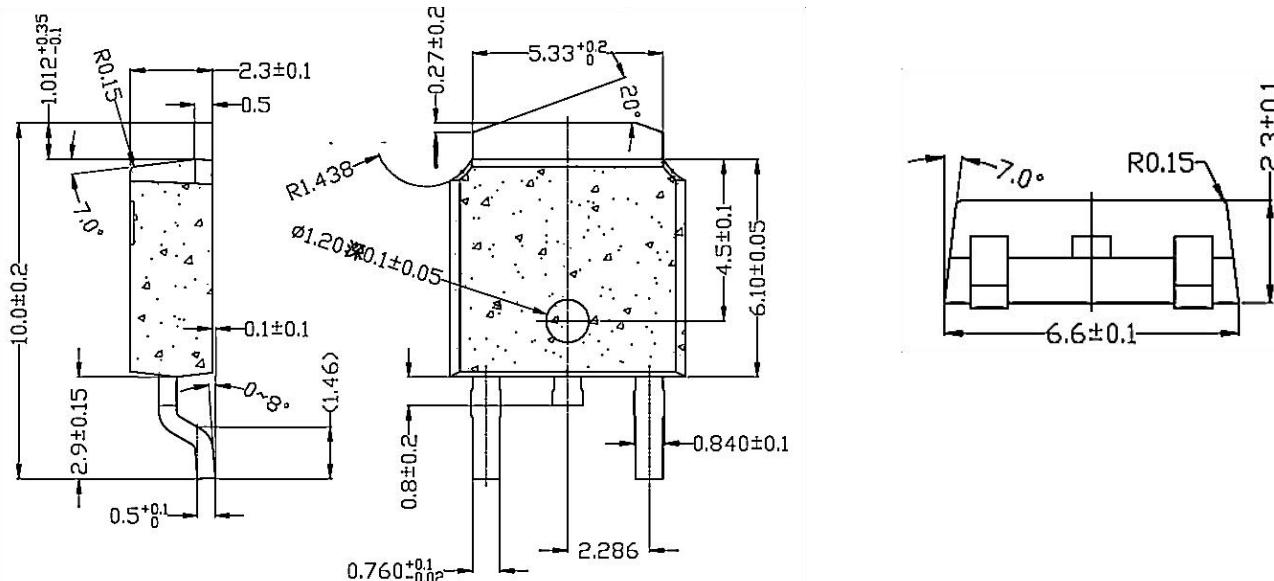
Package version	Tube dimensions LxWxH (mm)	Per Tube (pcs)	Tube per box	Inner box dimensions LxWxH (mm)	PCS/Inner box	Outer box dimensions LxWxH(mm)	PCS/Outer box



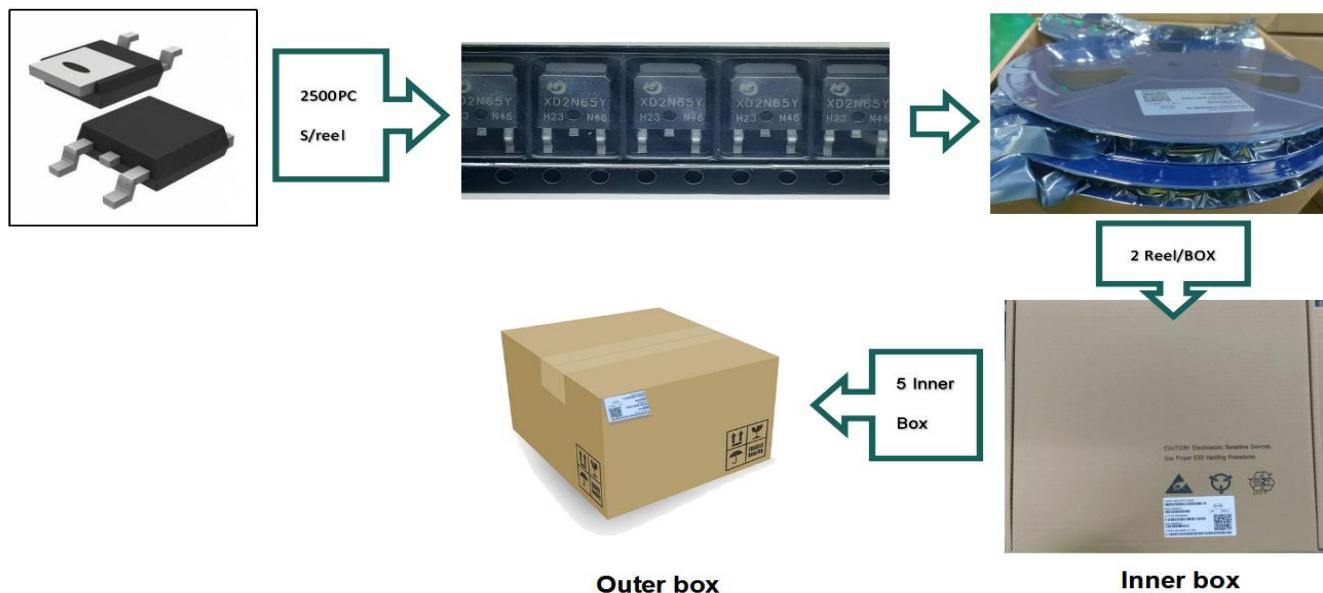
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### ■ TO - 252 PACKAGE OUTLINE DIMENSIONS



### ■ TO - 252 PACKING INFORMATION



Package version	Reel dimensions $\Phi \times H$ (mm)	Per Reel (pcs)	Reels per box	Inner box dimensions L×W×H (mm)	Outer box (pcs)	Outer box dimensions L×W×H (mm)
TO-252	$\Phi 330 \times 20$	2500	2	360*340*50	25000	375*375*280