



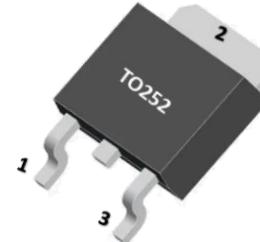
M65R380D

N-CHANNEL POWER MOSFET

11A, 650V N-CHANNEL SUPER-JUNCTION POWER MOSFET

■ DESCRIPTION

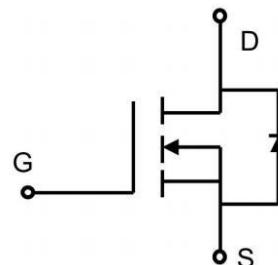
M65R380D This N-Channel enhancement mode power MOSFET is produced using advanced Super Junction technology. It achieves low conduction loss and switching losses. It leads the design engineers to their power converters with high efficiency, high power density, and superior thermal behavior. Furthermore, it's universal applicable, i.e., suitable for hard and soft switching topologies.



■ FEATURES

- * V_{DS}=650V, I_D= 11A
- * R_{DS(on)} (max.) =380 mΩ@V_{GS}= 10V
- * New revolutionary high voltage technology
- * Ultra low gate charge
- * High peak current capability

■ SYMBOL



■ APPLICATIONS

- * Switch Mode Power Supply (SMPS)
- * Uninterruptible Power Supply (UPS)
- * Power Factor Correction (PFC)
- * LED lighting power

■ MARKING



: HY LOGO

M65R380D=Device Code

XXXX=Date Code

Solid Dot=Green molding compound

■ ABSOLUTE MAXIMUM RATINGS(TA=25°C, unless otherwise specified.)

SYMBOL	PARAMETER		VALUE	UNIT
V _{DS}	Drain-Source Voltagee(V _{GS} =0V)		650	V
V _{GS}	Gate-Source Voltagee		±20	V
V _{GS}	Gate-Source Voltagee(V _{DS} =0V), AC(f>1HZ)		±30	V
I _D	Continuous Drain Current		11	A
	TC= 100 °C	7		
I _{DM}	Drain Current Pulsed(Note 1)		44	A
P _D	Power Dissipation (TC=25 °C)		23	W
	-Derate above 25 °C		0.19	W/°C
E _A S	Single Pulsed Avalanche Energy (Note 2)		166	mJ



M65R380D

N-CHANNEL POWER MOSFET

SYMBOL	PARAMETER	VALUE	UNIT
dv/dt	Reverse Diode dv/dt, VDS≤480V (Note 3)	15	V/ns
dVds/dt	Drain Source Voltage Slope, VDS≤480V	50	V/ns
TJ	Operation Junction Temperature Range	-55 to +150	°C
TSTG	Storage Temperature Range	-55 to +150	°C

Note: EAS condition: VDD=20V, L=0.5mH, RG=25Ω, Starting TJ = 25°C

■ THERMAL CHARACTERISTICS

SYMBOL	CHARACTERISTICS	MAX	UNIT
R _{θJC}	Thermal Resistance, Junction-to-Case	1.3	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62.5	°C/W
T _{sold}	Soldering Temperature, Wave Soldering only Allowed at Leads. (1.6mm from Case for 10s)	260	°C

■ ELECTRICAL CHARACTERISTICS (TA=25°C, unless otherwise specified)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Off Characteristics						
Drain -Source Breakdown Voltage	BVDSS	VGS=0V, ID=250uA	650	--	--	V
Drain-Source Leakage Current	IDSS	VDS=650V , VGS=0V TJ = 25°C	--	--	1.0	uA
Gate-Source Leakage Current	IGSS	VGS=+20V,VDS=0V	--	--	100	nA
Gate-Source Leakage Current	IGSS	VGS=-20V,VDS=0V	--	--	-100	nA
On Characteristics						
Gate Threshold Voltage	VGS(th)	VGS=VDS , ID=250uA	2.5	3.7	4.5	V
Static Drain- Source On State Resistance	RDS(on)	VGS= 10V, ID=5.5A	--	0.32	0.38	Ω
Gate resistance	Rg	f= 1MHz	--	3.1	--	Ω
Dynamic Characteristics						
Input Capacitance	Ciss	VDS=100V, VGS=0V f= 1MHz	--	678	--	pF
Output Capacitance	C0ss		--	30	--	
Reverse Transfer Capacitance	Crss		--	0.9	--	
Switching Characteristics						
Turn-on Delay Time	td(on)	VDD=325V, VGS=10V RG=25Ω ID= 11A,(Note 4,5)	--	14	--	ns
Turn-on Rise Time	tr		--	36	--	
Turn-off Delay Time	td(off)		--	48	--	
Turn-off Fall Time	tf		--	28	--	
Total Gate Charge	Qg	VDD=520V, VGS=0 to 10V ID= 11A (Note 4,5)	--	20	--	nC
Gate-Source Charge	Qgs		--	5.7	--	
Gate-Drain Charge	Qgd		--	10	--	



M65R380D

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CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
Continuous Source Current	I _S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	11	A
Pulsed Source Current	I _{SM}		--	--	44	
Diode Forward Voltage	V _{SD}	I _S = 11A, V _{GS} =0V	--	0.9	1.4	V
Reverse Recovery Time	T _{rr}	I _S = 11A, V _{GS} =0V	--	276	--	ns
Reverse Recovery Charge	Q _{rr}	d I _F /dt= 100A/μs	--	3.34	--	μC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. EAS condition: Starting TJ=25 °C, VDD=50V, VG= 10V, RG=25ohm.
3. ISD≤ID, di/dt ≤ 200A/us, VDD ≤ BVDSS , Starting TJ =25 °C .
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%.
5. Essentially Independent of Operating Temperature Typical Characteristics.

TYPICAL CHARACTERISTICS

Fig.1: Output Characteristics

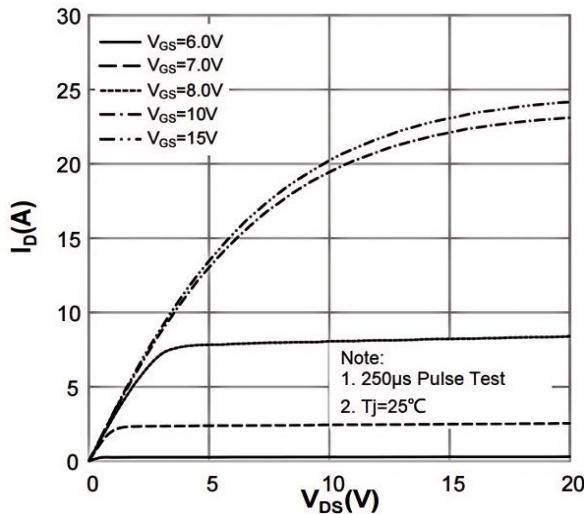


Fig.2: Typical Transfer Characteristics

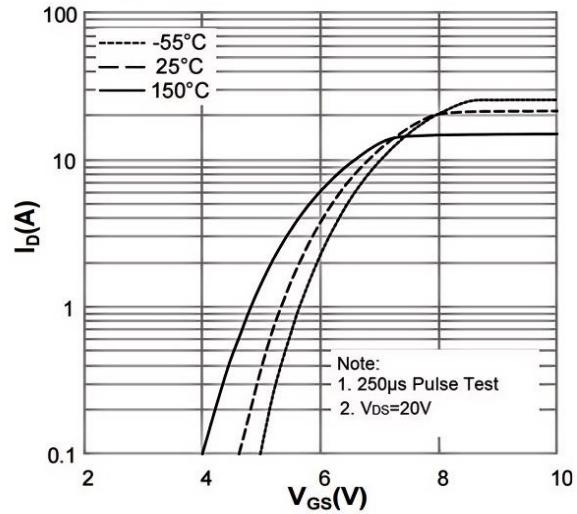


Fig.3: Typical On-resistance vs. Drain Current

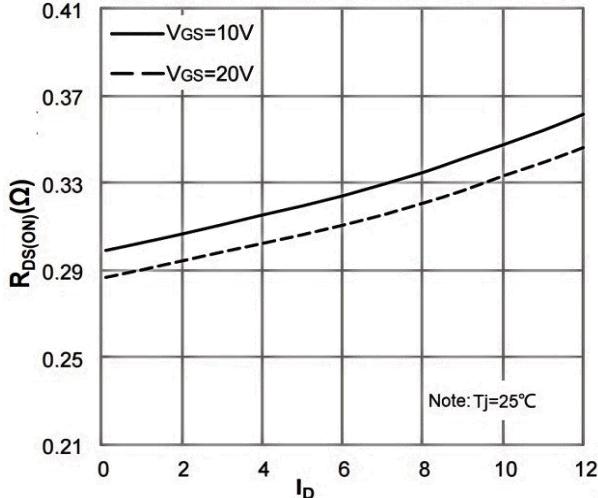
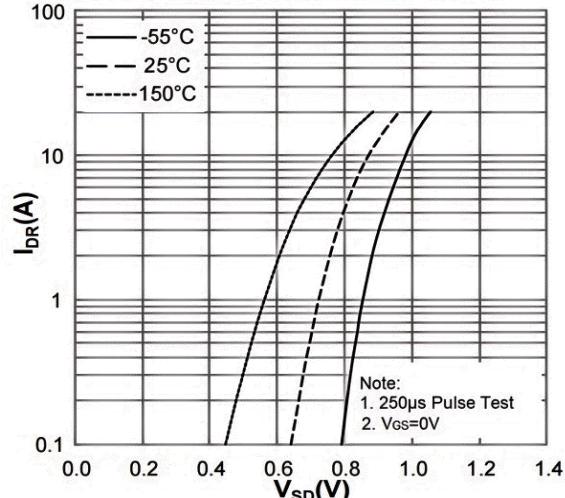


Fig.4: Typical Body Diode Forward Voltage

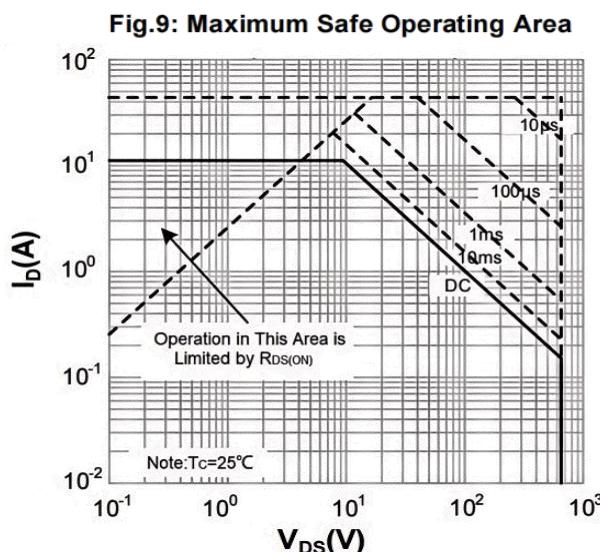
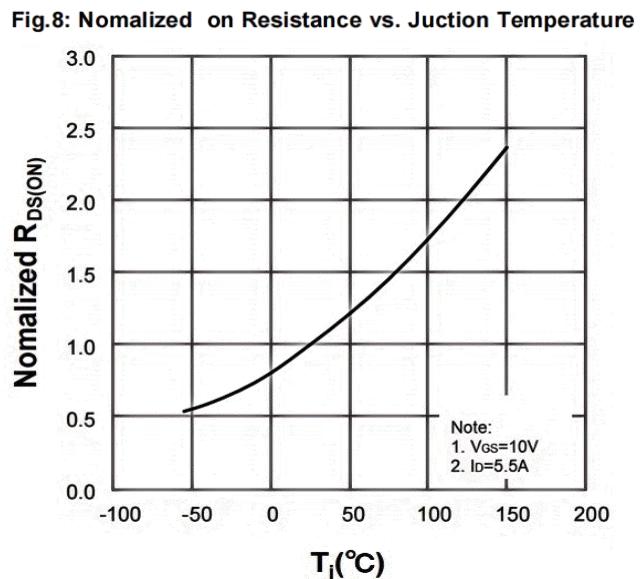
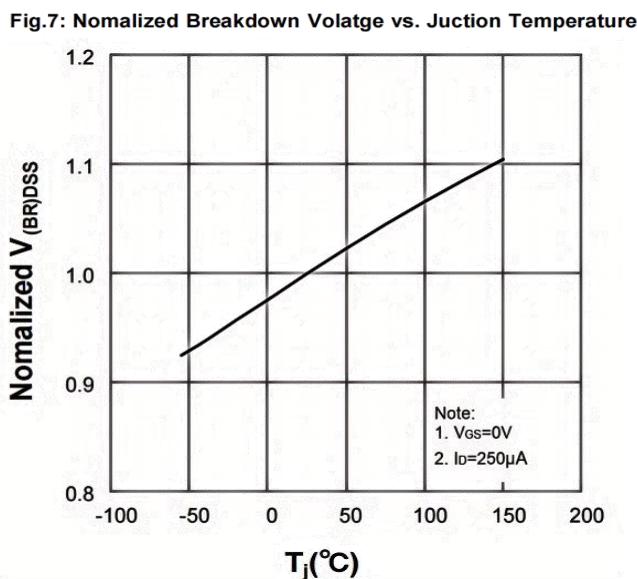
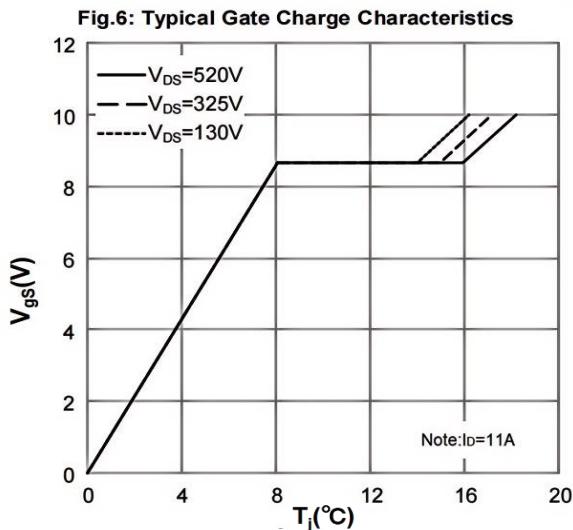
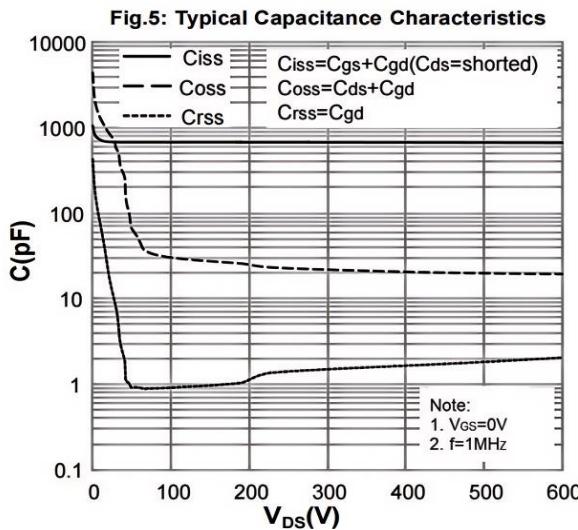




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TYPICAL CHARACTERISTICS(Con.t)



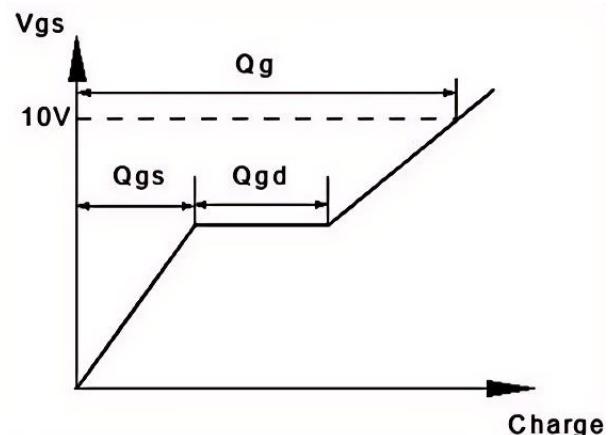
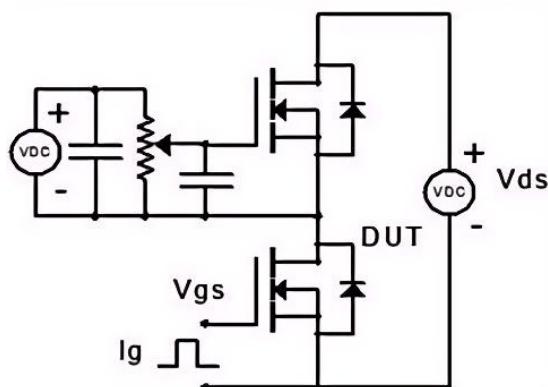


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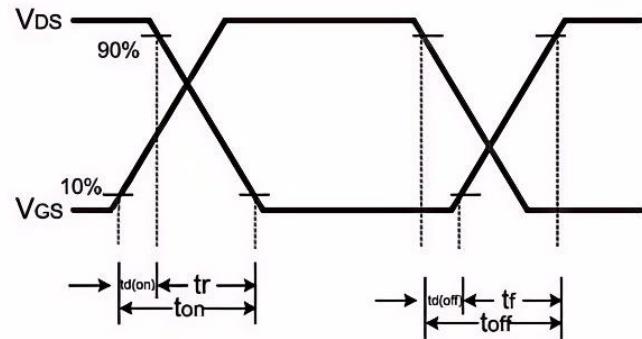
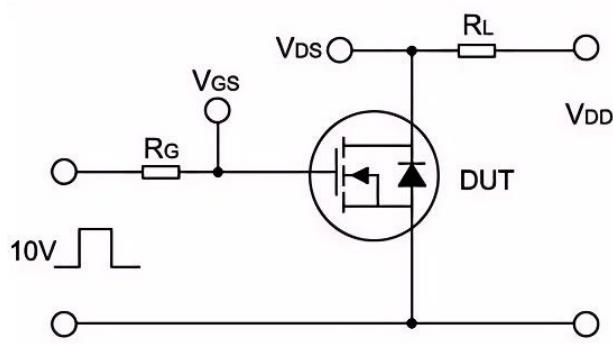
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TEST CIRCUIT

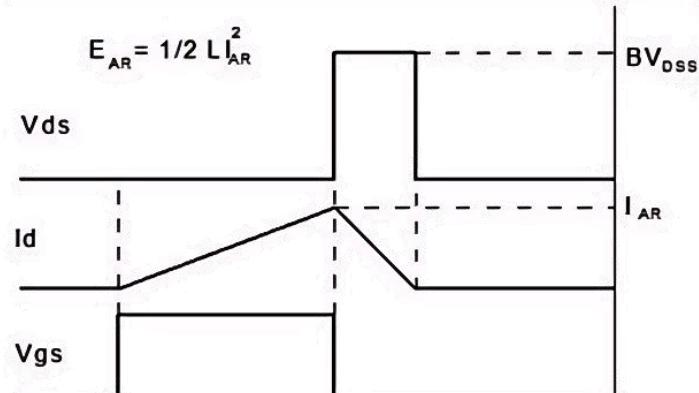
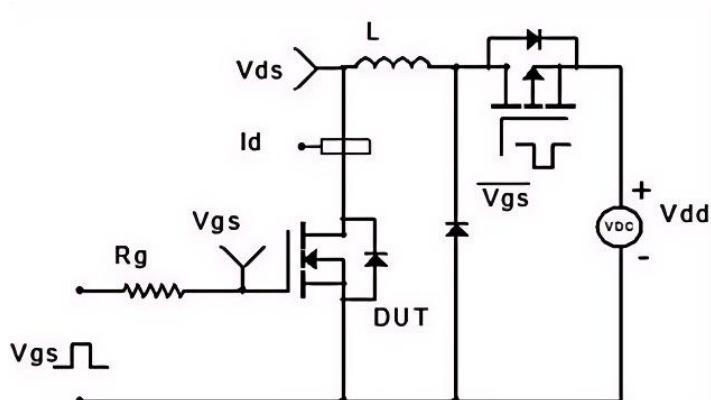
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform

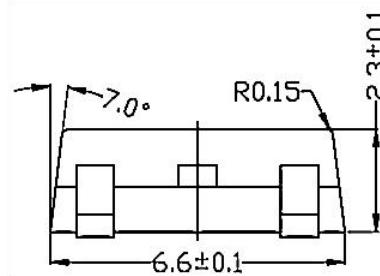
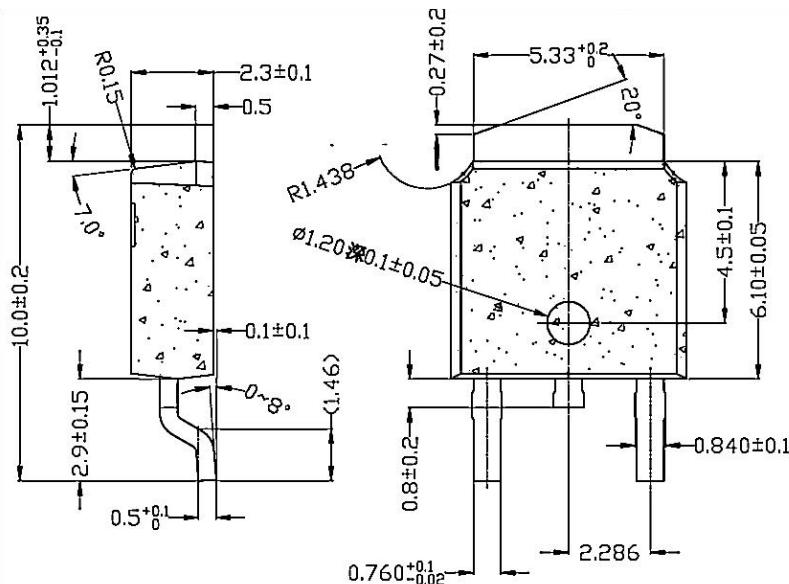




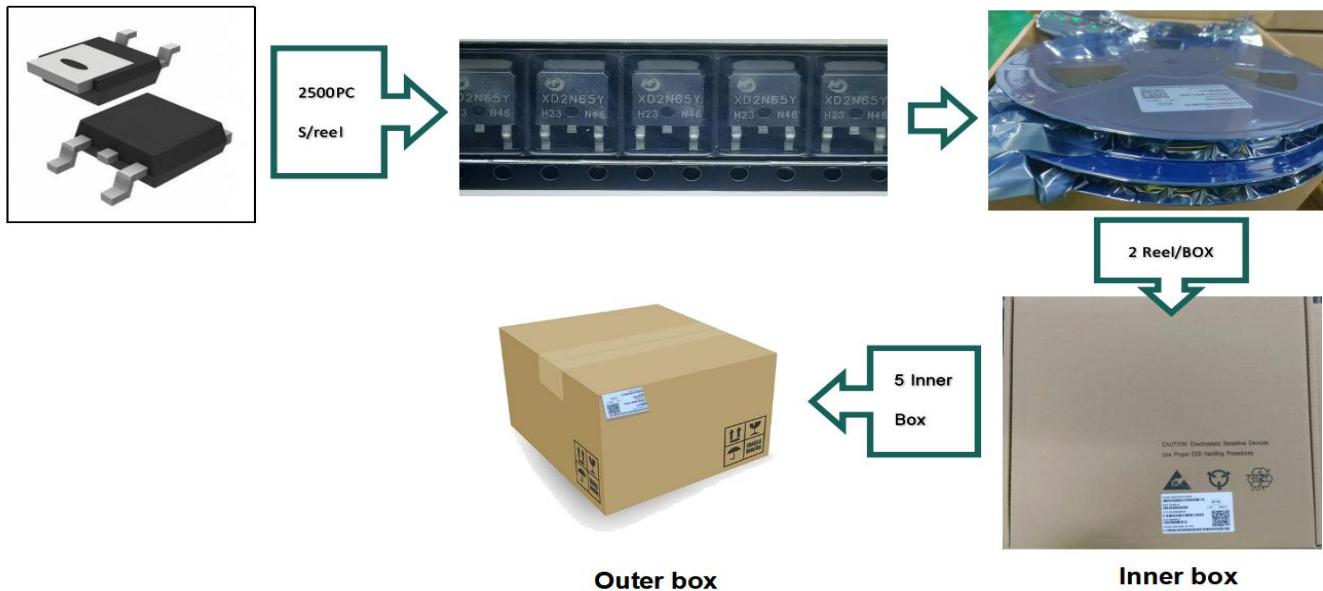
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■ TO - 252 Package Outline Dimensions



■ TO - 252 Packing Information



Package version	Reel dimensions $\Phi \times H$ (mm)	Per Reel (pcs)	Reels per box	Inner box dimensions L×W×H (mm)	Outer box (pcs)	Outer box dimensions L×W×H (mm)
T0-252	$\Phi 330 \times 20$	2500	2	360×340×50	25000	375×375×280