



DESCRIPTION

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.



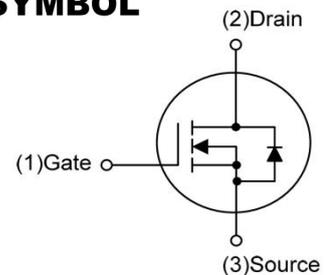
FEATURES

- * Very low FOM $R_{DS(on)} \times Q_g$
- * 100% avalanche tested
- * RoHS compliant

APPLICATIONS

- * Uninterruptible Power Supply (UPS)
- * Power Factor Correction (PFC)
- * Charger

SYMBOL



ABSOLUTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified.)

Parameter		Symbol	Value	Unit
Drain-Source Voltage (note2)		VDSS	650	V
Continuous Drain Current	TC = 25°C	ID	20	A
	TC = 100°C		12	
Pulsed Drain Current (note1)		IDM	53	A
Gate-Source Voltage		VGSS	±30	V
Single Pulse Avalanche Energy (note2)		EAS	103	mJ
Drain Source voltage slope (Vds=480V) (note3)		dV/dt	100	V/ns
Power Dissipation	TO-220F	PD	36	W
Operating Junction and Storage Temperature Range		TJ, Tstg	-55~+150	°C
Thermal Resistance, Junction-to-Case		RthJC	3.47	°C/W
Thermal Resistance, Junction-to-Ambient		RthJC	80	°C/W

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. IAS = 4A, VDD = 50V, RG = 25Ω, Starting TJ = 25 °C.



■ ELECTRICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Off&On Characteristics						
Drain-Source Breakdown Voltage	V(BR)DSS	V _{GS} = 0V, I _D = 250μA	650	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 650V, V _{GS} = 0V T _J = 25°C	--	--	1	μA
		V _{DS} = 650V, V _{GS} = 0V T _J = 150°C	--	--	100	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.5	--	4.5	V
Drain-Source On-State-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 8.5A	--	150	180	mΩ
Dynamic&Switching Characteristics						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 400V f = 100KHz	--	1230	--	pF
Output Capacitance	C _{oss}		--	35	--	
Reverse Transfer Capacitance	C _{rss}		--	--	--	
Total Gate Charge	Q _g	V _{DD} = 400V, I _D = 8.5A V _{GS} = 10V (Note 4)	--	57	--	nC
Gate-Source Charge	Q _{gs}		--	13	--	
Gate-Drain Charge	Q _{gd}		--	21	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 400V, I _D = 8.5A R _G = 10Ω, V _{GS} = 10V	--	15	--	ns
Turn-on Rise Time	t _r		--	11	--	
Turn-off Delay Time	t _{d(off)}		--	71	--	
Turn-off Fall Time	t _f		--	10	--	
Drain-Source Body Diode Characteristics						
Body Diode Forward Voltage	V _{SD}	T _J = 25°C, I _{SD} = 8.5A V _{GS} = 0V	--	0.9	1.4	V
Reverse Recovery Time	t _{rr}	V _R = 400V, I _F = 8.5A diF/dt = 100A/μs	--	284	--	ns
Reverse Recovery Charge	Q _{rr}		--	3.34	--	μC

Notes:

1. I_{SD} ≤ I_D, di/dt ≤ 200A/μs, V_{DD} ≤ B_VD_{SS}, Starting T_J = 25 °C.
2. Essentially Independent of Operating Temperature Typical Characteristics



■ TEST CIRCUITS AND WAVEFORMS

Figure A: Gate Charge Test Circuit and Waveform

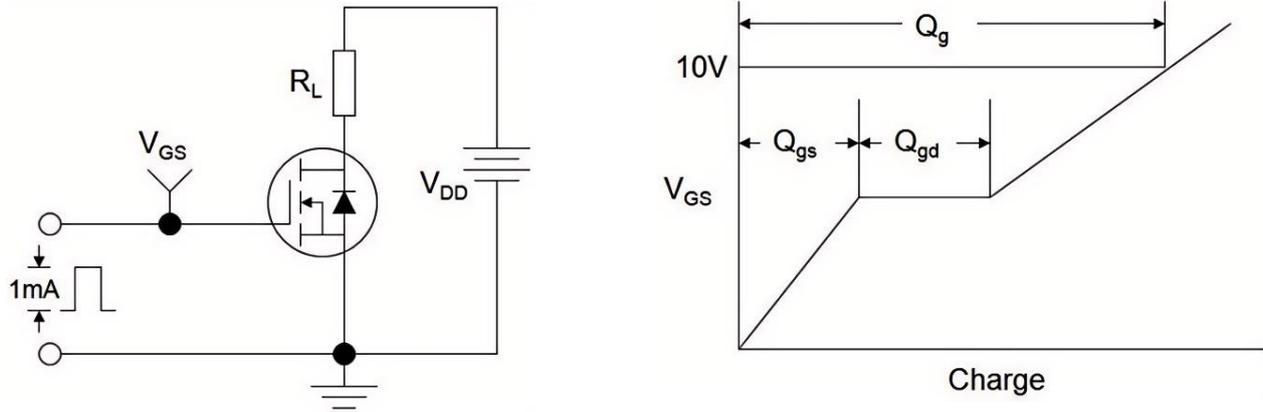


Figure B: Resistive Switching Test Circuit and Waveform

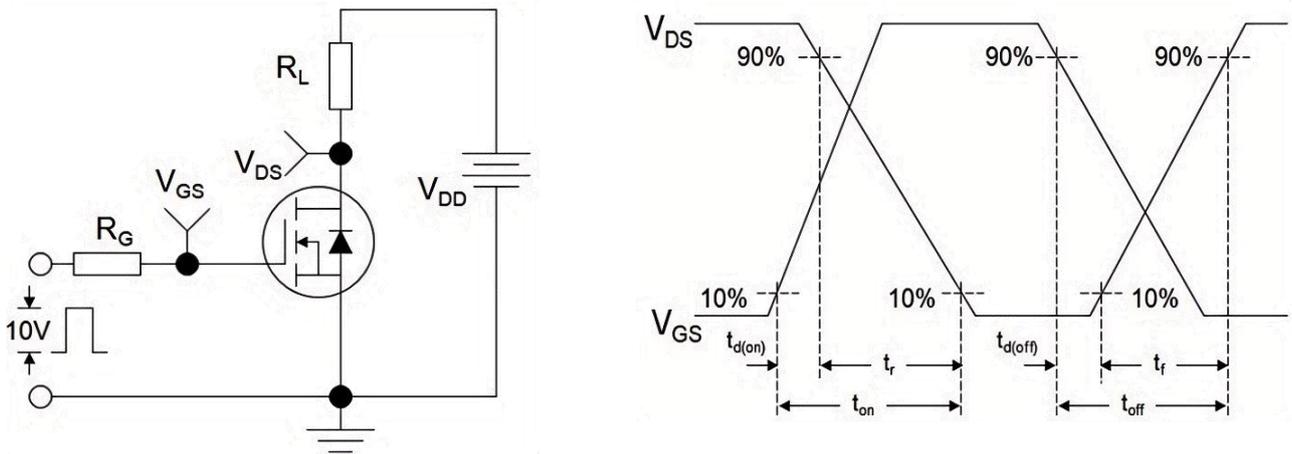
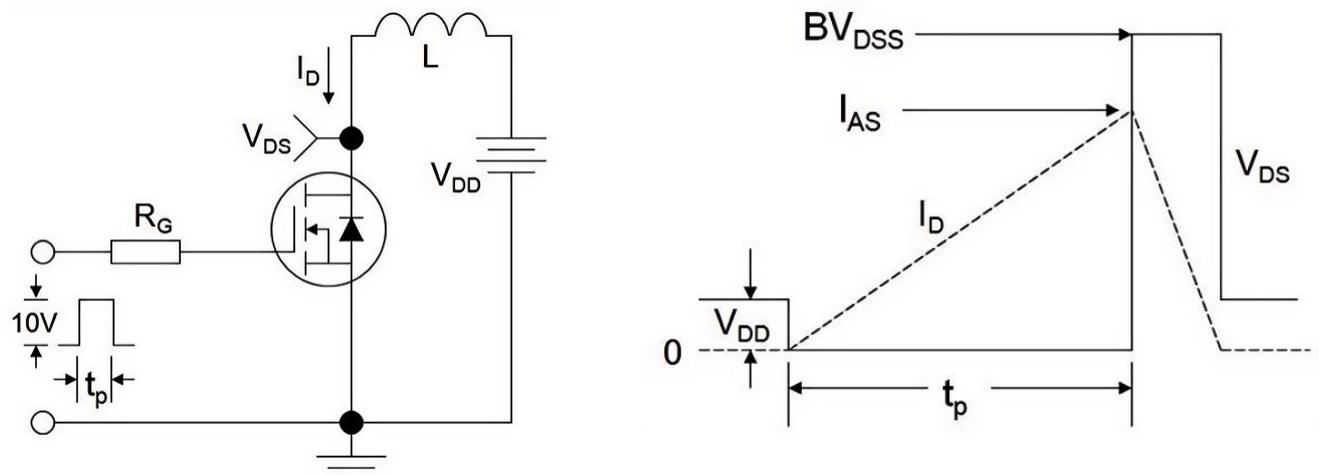


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





■ TYPICAL CHARACTERISTICS (T_J = 25°C, unless otherwise noted)

Figure 1. Output Characteristics

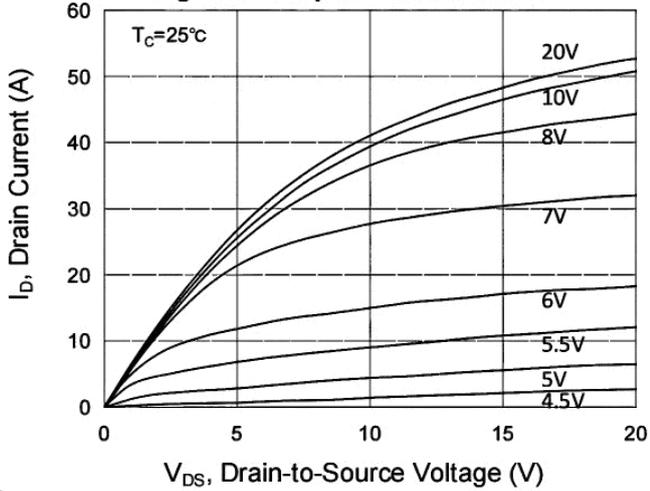


Figure 2. Transfer Characteristics

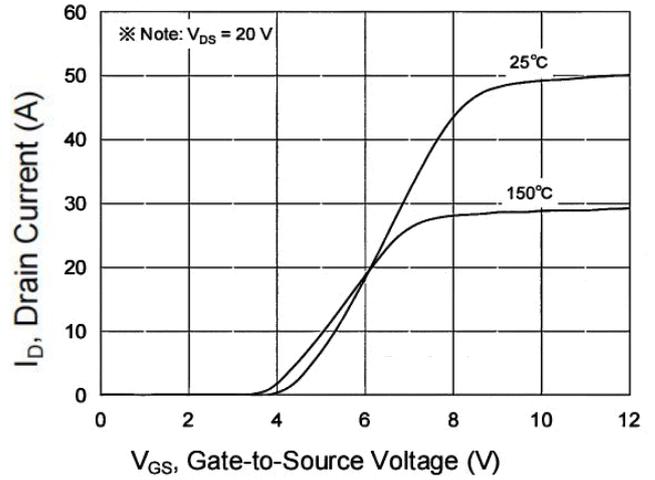


Figure 3. On-Resistance vs. Drain Current

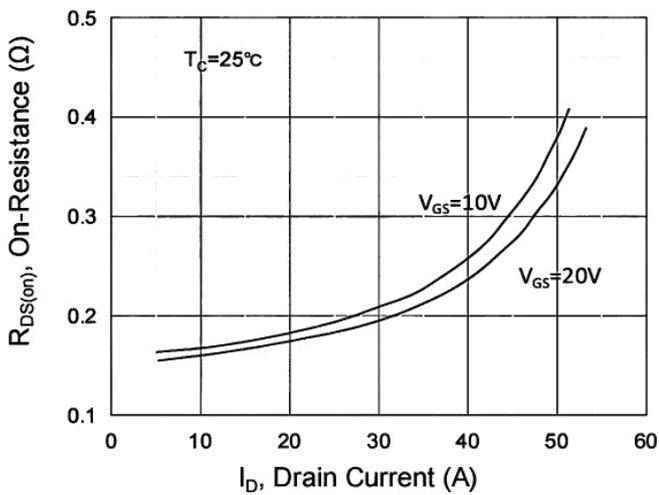


Figure 4. Capacitance

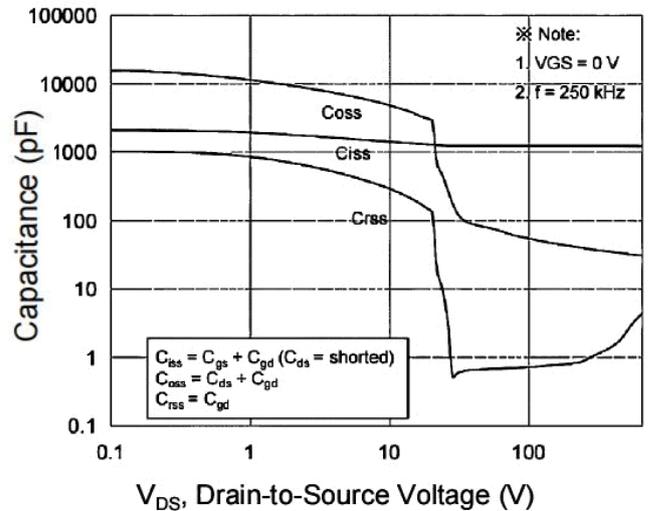


Figure 5. Gate Charge

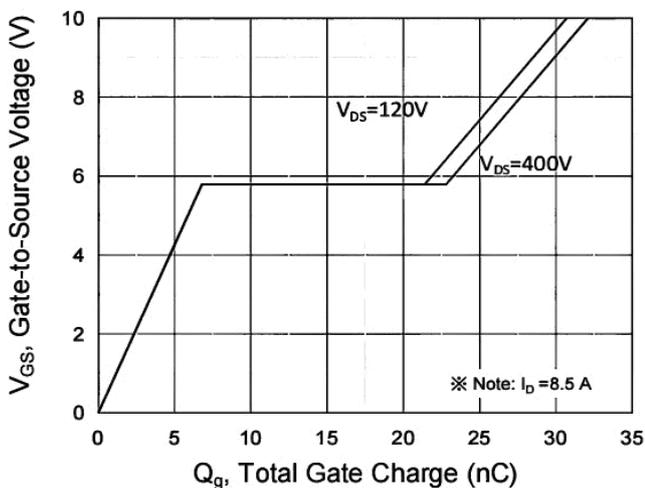
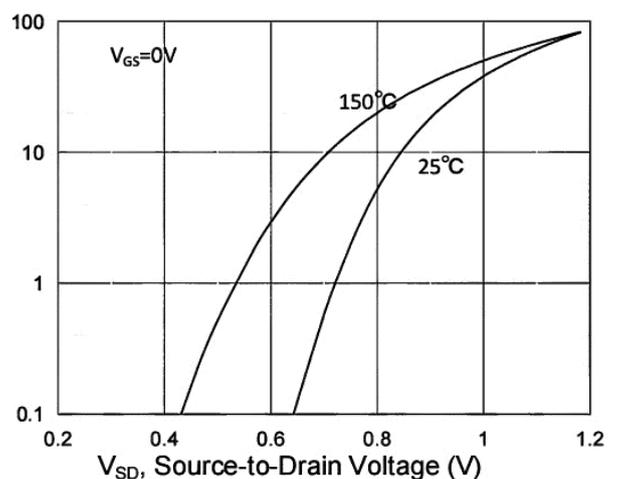


Figure 6. Body Diode Forward Voltage





■ TYPICAL CHARACTERISTICS(Con.t)

Figure 7. On-Resistance vs. Temperature

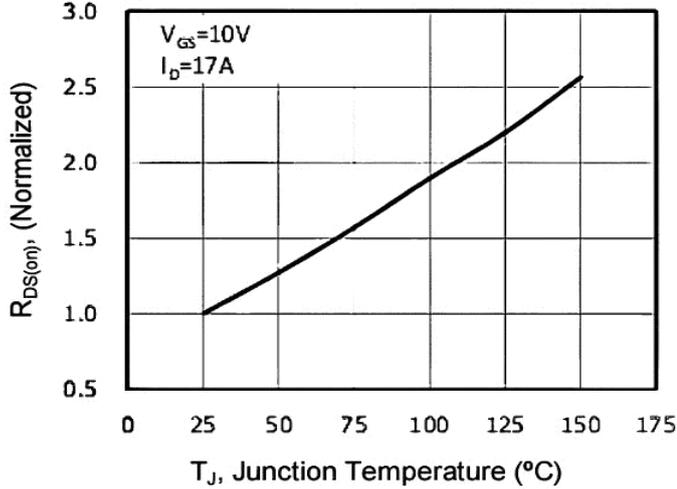


Figure 8. Breakdown Voltage vs. Junction Temperature

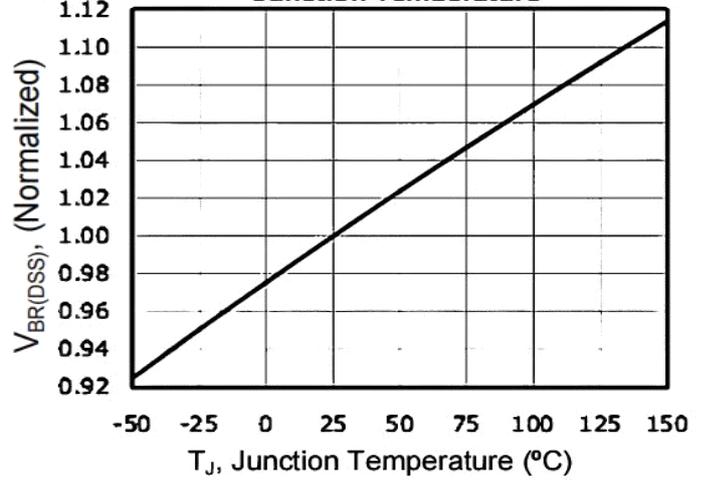


Figure 9. Transient Thermal Impedance TO-263/TO-220/TO-247 Figure 10. Safe Operation Area TO-263/TO-220/TO-247

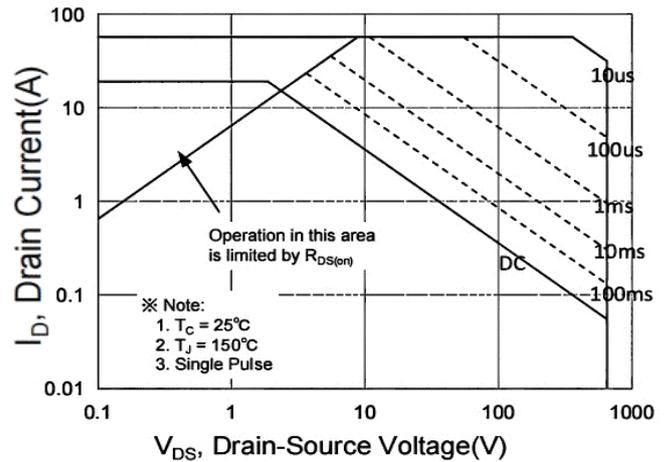
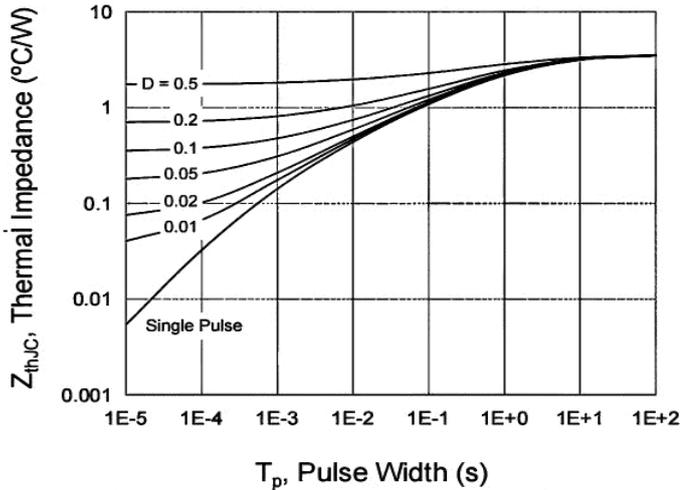


Figure 11. Transient Thermal Impedance TO-220F

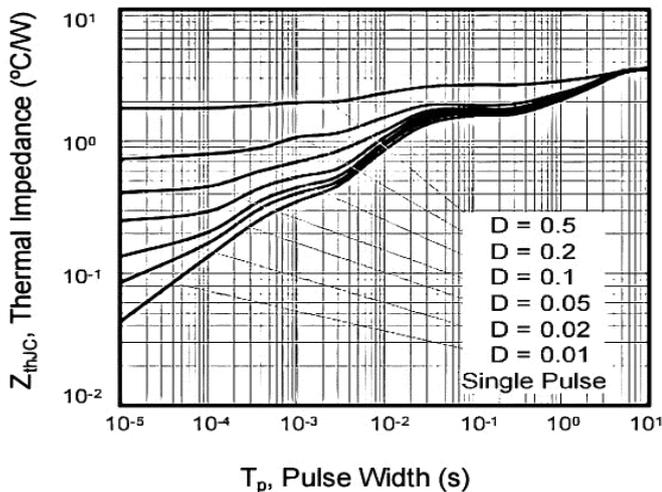
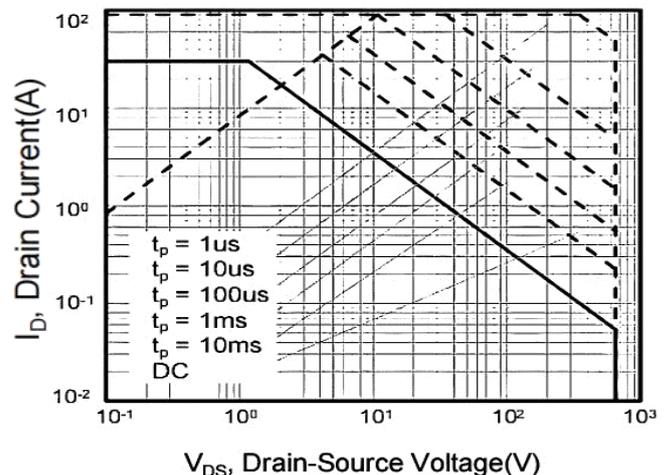


Figure 12. Safe Operation Area TO-220F

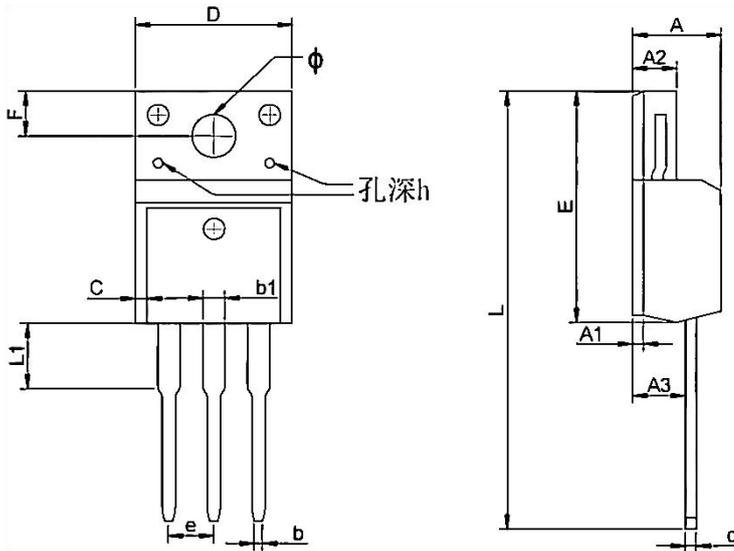




VMA65R180AD, VMB65R180AD, VMP65R180AD, VMW65R180AD

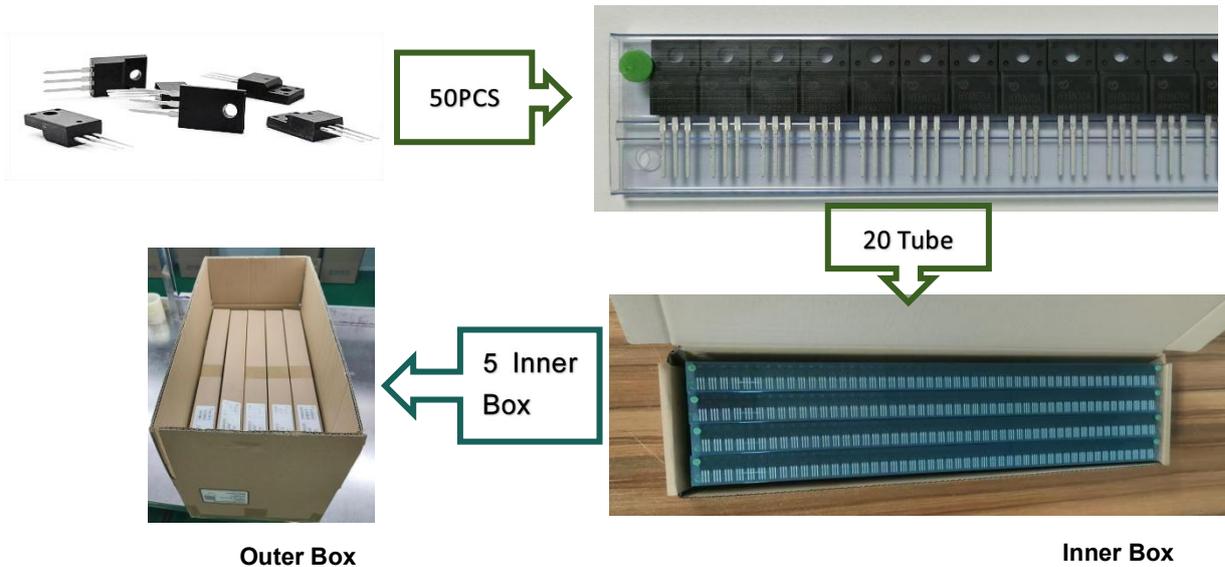
N-CHANNEL POWER MOSFET

TO-220F PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.300	4.750	0.169	0.185
A1	1.830 REF		0.072 REF	
A2	2.300	2.850	0.090	0.112
A3	2.500	2.900	0.098	0.114
b	0.400	0.420	0.016	0.016
b1	1.220	1.280	0.048	0.050
C	0.690	0.720	0.027	0.028
c	0.490	0.510	0.019	0.020
D	9.960	10.200	0.392	0.400
E	15.000	15.950	0.588	0.625
e	2.574 TYP		0.101TYP	
F	3.470 REF		0.136 REF	
y	3.200 REF		0.125 REF	
h	0.000	0.300	0.000	0.012
L	28.780	28.900	1.128	1.133
L1	2.990	3.100	0.117	0.122

TO - 220F PACKING INFORMATION



Outer Box

Inner Box

Package version	Tube dimensions LxWxH (mm)	Per Tube (pcs)	Tube per box	Inner box dimensions LxWxH (mm)	PCS/ Inner box	Outer box dimensions LxWxH(mm)	PCS/ Outer box
TO-220F	530*32*7	50	20	580*155*50	1000	602*277*188	5000